Appl. No.: 10/588,393

Amdt. dated February 10, 2009

Reply to Office Action of February 2, 2009

Amendments to the Claims:

1. (Original) A system for driving a compressor, comprising

an induction motor (2) for driving the compressor (3a), said induction motor including a squirrel cage rotor, and

- a controller (1) for controlling the induction motor, said controller comprising
- a memory (47) for storing drive patterns for driving the induction motor,
- a first frequency generation means (43) for generating a field frequency based on a field command and/or
- a second frequency generation means (44) for generating a voltage frequency based on a voltage command,

wherein a drive pattern is extracted from the memory based on the generated frequency or frequencies.

- (Original) The system according to claim 1, wherein the controller comprises a processing means (42) for generating the field command and/or the voltage command based on an input request.
- (Original) The system according to claim 1, wherein the controller is adapted to distinguish between a steady state and a transient state of the induction motor.
- 4. (Original) The system according to claim 3, wherein the processing means (42) is adapted to generate the field command and/or the voltage command depending on the state of the induction motor.
- (Original) The system according to claim 4, wherein the field command and/or voltage command is generated based on look-up tables.
- (Original) The system according to claim 4, wherein the field command and/or voltage command is generated based on a model based control.

Appl. No.: 10/588,393

Amdt. dated February 10, 2009

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- (Original) The system according to claim 1, wherein the first and/or the second frequency generation means (43, 44) is a voltage controlled oscillator.
- 8. (Original) The system according to claim 1, further comprising a counter (45, 46) receiving the frequency output of the frequency generation means (43, 44),

wherein the counter is adapted to count a value based on the frequency of the frequency generation means, and

the frequency is used as an address for accessing the memory (47).

9. (Currently Amended) The system according to claim 1, wherein the first frequency generation means (43) for generating the field frequency and the second frequency generation (44) means for generating a voltage frequency is used, wherein

the memory (47) is accessed by using a first address and a second address, and the system further comprises a first counter (45) and a second counter (46), wherein

the first counter is adapted to count a value based on the frequency of the first frequency generation means, and the second counter is adapted to count a value based on the frequency of the second frequency generation means, wherein

the count value of the first frequency generation means is used as the first address, and the count value of the second frequency generation means is used-a <u>as the</u> second address.

- 10. (Original) The system according to claim 9, wherein one of the first and second addresses is a column address, and the other address of the first and second addresses is a line address.
- 11. (Currently Amended) The system according to any of the claims 1 to 10 claim 1, wherein the compressor (3a) is a part of a turbocharger (3).
- 12. (Currently Amended) The system according to-any-of-the-claims 1 to 10 claim 1, wherein the compressor is an electrically driven compressor.

Appl. No.: 10/588,393

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Reply to Office Action of February 2, 2009

13. (Currently Amended) The system according to any of the claims 1 to 10 claim 1, wherein the compressor is part of an electrically assisted turbocharger.